

Adaptive Low-Power Circuits For Wireless Communications (Analog Circuits And Signal Processing)

By Aleksandar Tasic;Wouter A. Serdijn;John R. Long

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Wouter A. Serdijn is the author of Low-Voltage Low-Power Analog Integrated Circuits (0.0 avg rating, 0 ratings, 0 reviews, published 1995),

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Key Publications : US Patent "Wideband Jammer Detector", S.Sen PhD Thesis, "Design of Process and Environment Adaptive Ultra Low Power Wireless Circuits and Systems"